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INFORMATION DISCLOSURE STATEMENT BY APPLICANT	Docket: 245-53434	
	Applicant: Tenca et al.	
	Filed: July 21, 2000	Art Unit: 2766

OTHER DOCUMENTS

u			Kaliski, Jr., B.S., "The Montgomery Inverse and Its Applications," <u>IEEE Trans. on Computers</u> 44:1064-1065 (August 1995)
			Montgomery, P.L., "Modular Multiplication Without Trial Division," <u>Math. of Computation</u> 44:519-521 (April 1985)
			Koç, Ç.K. et al., "Analyzing and Comparing Montgomery Multiplication Algorithms," <u>IEEE Micro</u> 16:26-33 (June 1996)
			Dhem, J. et al., "SCALPS: Smart Card For Limited Payment Systems," <u>IEEE Micro</u> 16:42-51 (June 1996)
			Diffie, W., Hellman, M.E., "New Directions in Cryptography," <u>IEEE Trans. on Information Theory</u> 22:644-654 (1976)
			Rivest, R.L. et al., "A Method for Obtaining Digital Signatures and Public-Key Cryptosystems," <u>Communications of the ACM</u> 21:120-126 (1978)
			Koç, Ç.K., Acar, T., "Fast Software Exponentiation in $GF(2^k)$ " in <u>Proceedings, 13th Symposium on Computer Arithmetic</u> , pp. 225-231 (July 1997) (T. Lang et al., editors)
			Hamano, T. et al., " $O(n)$ -Depth Circuit Algorithm for Modular Exponentiation" in <u>Proceedings, 12th Symposium on Computer Arithmetic</u> , pp. 188-192 (July 1995) (S. Knowles, W.H. McAllister, editors)
m			Orup, H., "Simplifying Quotient Determination in High-radix Modular Multiplication" in <u>Proceedings, 12th Symposium on Computer Arithmetic</u> , pp. 193-199 (July 1995) S. Knowles, W.H. McAllister, editors)

EXAMINER:

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11/15/06

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OTHER DOCUMENTS

EW			Bernal, A., Guyot, A., "Design of a Modular Multiplier Based on Montgomery's Algorithm" in <u>13th Conference on Design of Circuits and Integrated Systems</u> , pp. 680-685 (November 1998)
			Eldridge, S.E., Walter, C.D., "Hardware Implementation of Montgomery's Modular Multiplication Algorithm," <u>IEEE Trans. Computers</u> 42:693-699 (June 1993)
			Kornerup, P., "High-Radix Modular Multiplication for Cryptosystems" in <u>Proceedings, 11th Symposium on Computer Arithmetic</u> , pp. 277-283 (June 1993) (E. Swartzlander et al., editors)
			Walter, C.D., "Space/Time Trade-offs for Higher Radix Modular Multiplication Using Repeated Addition," <u>IEEE Trans. Computers</u> 46:139-141 (1997)
			Royo, A., et al., "Design and Implementation of a Coprocessor for Cryptography Applications," <u>European Design and Test Conference</u> , pp. 213-217 (March 1997)
			Koç, Ç.K., Acar, T., "Montgomery Multiplication in GF(2k), " <u>Designs, Codes and Cryptography</u> 14:57-69 (1998)
EW			Tenca, A.F., "Variable Long-Precision Arithmetic (VLPA) for Reconfigurable Coprocessor Architectures," Ph.D. Thesis, University of California at Los Angeles (March 1998)

EXAMINER: <i>EW</i>	DATE: 1/15/06
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